

INFORMATION DISCLOSURE STATEMENT

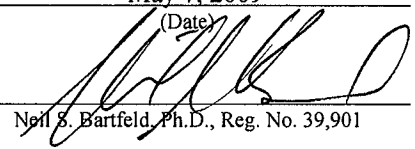
Applicant : Ayaki et al.
App. No : 10/582,190
Filed : March 21, 2007
For : TAPE WITH BUILT-IN IC CHIP AND
SHEET WITH BUILT-IN IC CHIP
Examiner : Brent T. O'Hern
Art Unit : 1794

**CERTIFICATE OF EFS WEB
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I hereby certify that this correspondence, and any other attachment noted on the automated Acknowledgement Receipt, is being transmitted from within the Pacific Time zone to the Commissioner for Patents via the EFS Web server on:

May 7, 2009

(Date)


Neil S. Bartfeld, Ph.D., Reg. No. 39,901

Mail Stop Amendment
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Dear Sir:

Enclosed for filing in the above-identified application is a PTO/SB/08 Equivalent listing 3 (three) references to be considered by the Examiner. Also enclosed are 3 (three) foreign patent references and/or non-patent literature as listed on the Information Disclosure Statement.

This Information Disclosure Statement is being filed before the mailing date of a final action and before the mailing date of a Notice of Allowance.

CERTIFICATION UNDER 37 C.F.R. § 1.97(e)(1)

I hereby certify that each item of information contained in this Statement was first cited in any communication from a foreign Patent Office in a counterpart foreign application not more than three months prior to the filing of this Information Disclosure Statement.

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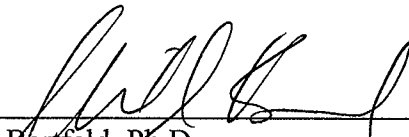
Docket No. SHIGA3.010APC
Customer No. 20,995

Thus, no fee is required as set forth in 37 C.F.R. § 1.97(c), however, the Commissioner is hereby authorized to charge any additional fees which may be required or to credit any overpayment to Account No. 11-1410.

Respectfully submitted,

KNOBBE, MARTENS, OLSON & BEAR, LLP

Dated: 5/7/09

By: 
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